

DUMPS ARENA

HCIA-Intelligent Computing V1.0

Huawei H13-211

Version Demo

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QUESTION NO: 1

Which of the following are not typical GPU application scenarios?

- A. General calculation, complete complex logic operation
- B. Video image processing scene
- C. HPC hybrid application high throughput scenario
- D. HPC high-performance acceleration scenarios

ANSWER: A**QUESTION NO: 2**

The following is a positive solution to the LVS+tomcat cluster model.

- A. LVS-NAT. Network Address Translation, modify the target IP of the request message
- B. Like this load scheduling technology implemented in the Liunx kernel, we call it the Linux Virtual Server (LVS)
- C. LVS-DR:
- D. LVS-TUN:

ANSWER: A B C D**QUESTION NO: 3**

What are the common deployment models of cloud computing.

- A. Hybrid cloud computing
- B. Community cloud computing
- C. Public cloud computing
- D. Private cloud computing

ANSWER: A B C D

QUESTION NO: 4

PAAS provides an application development and operating environment, including a database of middleware.

The software below the server upper layer belongs to the PAAS layer.

- A. KVM(LAAS)
- B. MYSQL
- C. CRM (SAAS)
- D. JAVA

ANSWER: B D

QUESTION NO: 5

The three types of operating systems divided by application areas are () server operating systems and (), where server operating systems generally refer to installations on mainframe computers. On the operating system, such as eb server, application server and database server.

- A. embedded operating system
- B. open source operating system
- C. smart card operating system
- D. Desktop operating system

ANSWER: A D

QUESTION NO: 6

ARM microprocessors have been used in various product markets such as industrial control, consumer electronics, communication systems, network systems, and wireless systems. The following description about the characteristics of the ARM architecture is wrong.

- A. Supports 16-bit, 32-bit, and 64-bit instruction sets, and is well compatible with various IOT, terminal to cloud application scenarios
- B. Use a complex instruction set. High processing efficiency
- C. Extensive use of registers. Most data operations are done in registers, and instruction execution is faster
- D. The same function and performance occupy a small chip area, low power consumption, higher integration, and more hardware CPU cores have better concurrent performance

ANSWER: B**QUESTION NO: 7**

Middleware is mainly to provide running and development environment for upper-layer application software, provide pre-made and reusable business function modules, API interface, etc. to help users flexibly and efficiently develop and integrate complex application systems.

- A. TRUE
- B. FALSE

ANSWER: A**QUESTION NO: 8**

Memory is an important part of the computer. It is a bridge to communicate with the CPU. The real change of internal mining performance affects the overall performance of the server. With the rapid development of the server, please use memory to gradually evolve. The direction may be.

- A. The voltage is getting higher and higher
- B. Increasing capacity
- C. The frequency is getting higher and higher
- D. The power is getting higher and higher

ANSWER: A B C**QUESTION NO: 9**

CPU frequency is also called clock frequency. The unit is megahertz (MHz) or gigahertz (GHz), and its function is.

- A. Used to indicate the progress of CPU operations and data processing
- B. Determine the data transmission speed of the bus
- C. Determines the running speed of the whole motherboard
- D. Determine the speed of data exchange between CPU and memory

ANSWER: A**QUESTION NO: 10**

Heterogeneous computing is a new computing model after single-core computing and multi-core parallel computing. The following background description about heterogeneous computing is wrong.

- A.** Heterogeneous computing maximizes the performance of each processor, giving the most suitable task to the best computer unit
- B.** NUMA is a mode of screen structure calculation
- C.** Mob's law is getting closer to the physical limit, increasing the difficulty and cost of improving single-core performance
- D.** The performance improvement from multi-core practitioners is limited. With the increase in parallelism, the problems of heat dissipation and energy consumption are becoming increasingly prominent

ANSWER: B